

# X100 Pro-2U Video Splicer

Specification v1.0



### 1 Overview

X100 Pro-2U is Colorlight's brand-new professional video splicer, designed especially for large splicing screens. It integrates multiple video processing functions, such as cropping, scaling, splicing, and multi-screen display. This multipurpose device can serve as a processor for LCD and DLP splicing screens or as a controller for fine-pitch LED video walls with ultra-high resolution.

With a modular design and robust FPGA architecture, X100 Pro-2U delivers outstanding display effects and efficient video processing capability, ensuring long-term, stable, and safe operation. The modular plug-in design also allows users to flexibly configure input and output boards as needed, greatly satisfying the demands of different scenarios.

In terms of inputs, X100 Pro-2U offers industry-standard ports including HDMI, DP, SDI, DVI, VGA, CVBS, and RJ45, and supports 1080P HD and 4K resolution of 4096×2160@60Hz. As for outputs, it supports both Gigabit Ethernet (GbE) and 10 Gigabit optical fiber outputs, facilitating the smooth display of fine-pitch LED screens at large scales and over long distances. Additionally, DVI and HDMI outputs are available, enabling flexible application of LCD and DLP splicing screens.

Given its powerful features and superior performance, X100 Pro-2U is suitable for a wide range of applications, such as command and dispatch systems, power system operation and maintenance, party and government conferences, visualization data centers, broadcasting and television, as well as high-end stage rentals.



### 2 Appearance

#### 2.1 Front Panel



| No. | Name         | Description  |
|-----|--------------|--|
| 1   | Touch screen | Displays current device status and allows for parameters configuration and device operation. |
| 2   | Power switch | Power the device on/off.   |

Note: The image shown is for illustration purpose only and may not be an exact representation of the product due to hardware configuration and production process. Please refer to the actual product.

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### 2.2 Rear Panel



| No. | Name             | Description  |
|-----|------------------|--|
| 1   | Input board      | Supports 10 types of input boards.   |
| 2   | Mains Input      | AC 100-240V, 50/60Hz.  |
| 3   | Preview and      | Displays the preview of 1 channel of 2K input and monitoring   |
| 3   | monitoring board | of its real-time output.   |
| 4   | Main board       | <ul> <li>GENLOCK IN: Accepts the sync signal;</li> <li>GENLOCK LOOP: Loops the sync signal;</li> <li>RS232 serial port;</li> <li>USB 3.0 port;</li> <li>GbE control port;</li> <li>3D port.</li> </ul> |
| 5   | Output board     | Supports 7 types of output boards.   |

Note: The image shown is for illustration purpose only and may not be an exact representation of the product due to hardware configuration and production process. Please refer to the actual product.

#### **3** Features

#### Main Board

• GENLOCK IN/LOOP:

- 1×GENLOCK IN port, for Genlock signal input; supports Bi-Level and Tri-Level sync.

- -1×GENLOCK LOOP port, for Genlock signal output.
- RS232:

 $-1 \times RJ11/RS232$  serial port (baud rate: 115,200), for connecting to a central controller or other devices.

• USB:

 $-1 \times USB$  3.0 port, for upgrading the program and image file via a USB drive.

• LAN:

 $-1 \times RJ45$  GbE port, for connecting to a control PC for communication.

• 3D:

 $-1 \times 3D$  VESA port, for 3D sync signal output (Works with a 3D emitter and 3D glasses, both of which need to be purchased separately as optional accessories.)

#### Input

- 10 types of input boards available for flexible configuration:
  - $-1 \times HDM12.0$ , supports up to  $4096 \times 2160@60$ Hz input on a single channel.
  - $-1 \times DP1.2$ , supports up to  $4096 \times 2160@60Hz$  input on a single channel.
  - $-1 \times 12G$ -SDI, supports up to  $4096 \times 2160@60$ Hz video input on a single channel.
  - $-1 \times HDM12.0 + 1 \times DP1.2$  (either-or), supports up to  $4096 \times 2160@60Hz$  input on a single channel.
  - $-4 \times DVI$ , supports up to  $1920 \times 1200@60$ Hz input on a single channel.
  - $-4 \times HDM11.4$ , supports up to  $1920 \times 1200@60$ Hz input on a single channel.
  - -4×3G-SDI, supports up to  $1920 \times 1080@60$ Hz input on a single channel.
  - -4×VGA, supports up to  $1920 \times 1080@60$ Hz input on a single channel.
  - $2 \times VGA$  +  $2 \times CVBS$ . VGA supports up to  $1920 \times 1080@60Hz$  input on a single channel; CVBS supports PAL/NTSC video input.
  - $-\,2\!\times\!RJ45$  GbE ports for V\_IPX2, supports H.264 and H.265 decoding.
- Total number of input boards on a single device:
  - Maximum number of boards: 2.

- Maximum number of input windows: 32.

#### **Output**

- 7 types of output boards available for flexible configuration:
  - $-1 \times HDM12.0$ , supports up to  $4096 \times 2160@60$ Hz output on a single channel.
  - $-4 \times HDMI1.4$ , supports up to  $1920 \times 1200@60Hz$  output on a single channel.
  - -4×DVI, supports up to  $1920 \times 1200$ %0Hz output on a single channel.
  - $-8 \times GbE$  ports, with a maximum load capacity of 5.24 million pixels output.
  - -10 $\times$ GbE ports, with a maximum load capacity of 6.55 million pixels output.
  - $-4 \times 5$ G Ethernet ports (2 main and 2 backup), with a maximum load capacity of 5.89 million pixels output.
  - $-2 \times 10$  G fiber ports (1 main and 1 backup), with a maximum load capacity of 6.55 million pixels output.
- Preview and monitoring:
  - $-1 \times HDMI1.4$  port, for previewing inputs and monitoring real-time outputs, with
    - a fixed output of  $1920 \times 1080@60 \text{Hz}.$
  - Supports previewing inputs and monitoring real-time outputs via web-based software.
- Total number of output boards on a single device:
  - Maximum number of boards: 4.
  - Maximum number of video outputs: 16.
  - Maximum number of GbE output ports:  $40 \times GbE$  ports, with a maximum load capacity of 26.21 million pixels.
  - Maximum number of fiber output ports: 4 main and 4 backup 10G fiber ports, with a maximum load capacity of 26.21 million pixels.
- Limitations on a single device:
  - Maximum load capacity of a single device: 36,864,000 pixels.
  - Maximum width/height load capacity of a single device or a single layer: 16, 384 pixels (maximum width/height).
  - Limitations on using different types of boards: 4K and 2K video output boards cannot be used together.

#### Video Processing

- Number of input signals:
  - Supports  $2 \times 4K$  or  $8 \times 1080P$  simultaneously.

• Multi-window and multi-layer display:

- Supports window roaming and free splicing.

- Cropping:
  - Supports cropping of the input source. The cropped source can be used independently as a new input source.
- Scrolling text:
  - Supports customizing text content, and setting the font format and size, scrolling direction and speed, background color, etc.
  - -Flexible displaying of messages, notifications, slogans, and banners.
- UHD background:
  - Supports uploading high-resolution images for background display, with a maximum width/height of 32,767 pixels.
- Logo management for input:
  - Available for text or image.
- HDR:
  - Supports HDR10, compliant with SMPTE ST 2086/2084 standards.
  - Supports HLG.
- 3D display:
  - Works with a 3D emitter and active 3D glasses (optional accessories) to deliver a 3D visual experience.
  - Custom frame rate:
    - Available frame rates: 29.97/30/50/59.94/60/120Hz.
    - Supports customizing any frame rate within 23.98~240Hz.

#### Color Management

• Independent color adjustment of each input source, enabling adjustments to brightness, color temperature, RGB gain, hue, contrast, saturation, and brightness compensation.

• Independent color adjustment of each Ethernet output, enabling adjustments to brightness, color temperature, RGB gain, hue, contrast, saturation, and brightness compensation.

• Independent color adjustment of each video output, enabling adjustments to brightness, color temperature, and RGB gain.

• Brightness adjustment on the level of port group, enabling independent



management of display brightness for each group.

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#### **Device Control**

• Connectable to a PC and central controller via LAN, RS232, etc.

• Supports device access and control from Web using different operating systems (Windows, iOS, Android, Linux); multi-user operation supported.

• App control: Works with Colorlight's Kylin Visualization Intelligent Control

Platform.

- Accesses device information and performs operations on the front panel.
- Manages up to 2,000 presets and schedules the preset tours.

#### Easy Maintenance

• Upgrades the program and image file via a USB drive or web-based software.

#### Stable and Reliable

- Redundancy backup:
  - Supports redundancy backup of Ethernet output ports and fiber output ports on
    - a single device.
  - Supports inter-device redundancy backup.
- Device monitoring:
  - Abnormal temperature alarm, disconnection alert, etc.

### 4 Certifications

CCC, CE, UKCA, FCC, EAC, IC, and KC.

#### Certifications of CB, and cTUVus are underway.

Note: If the product does not have the relevant certifications required by the countries or regions where it is to be sold, please contact Colorlight to confirm or address the problem. Otherwise, the customer shall be responsible for the legal risks, or Colorlight has the right to claim compensation.

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## 5 Board Specifications

### 5.1 Input Board

| V4KH11NV5101: | 1×HDM  | 2.0 port                                |             |          |                |                                   |  |
|---------------|--|---|-------------|----------|----------------|-----------------------------------|--|
|               |  |   |             |          |                |                                   |  |
| Details       | <ul> <li>1×HDMI Type A port.</li> <li>HDM12.0 standard, compatible with HDM11.4/1.3.</li> <li>A single port supports a maximum resolution of 4096×2160@60Hz and a minimum of 800×600@60Hz; the maximum pixel clock is 594MHz.</li> <li>8/10/12bit input source.</li> <li>Custom resolutions: <ul> <li>Maximum width: 8,192 pixels (8192×1080@60Hz, forced by external signal, EDID unavailable).</li> <li>Maximum height: 8,192 pixels (1024×8192@60Hz, forced by external signal, EDID unavailable).</li> </ul> </li> <li>HDR supported.</li> <li>Independent EDID settings, adopting EDID V1.3 standard.</li> <li>HDCP2.2 compliant, backward compatible.</li> </ul> |   |             |          |                |                                   |  |
|               | Input  | rlaced input r<br>Maximum<br>resolution | Color space | Sampling | Color<br>depth | Frame rate (Hz)                   |  |
|               |  |   | YCbCr/RGB   | 4:4:4    | 12bit          | 23. 98, 24, 30                    |  |
|               |  | 4096×2160                               | YCbCr       | 4:2:2    | 8bit           |                                   |  |
|               |  | <u> </u>                                | YCbCr/RGB   | 4:4:4    | 8bit           | 23. 98, 24, 30, 50,               |  |
| Taskalasl     | 4K   | $\sim$                                  | YCbCr       | 4:2:2    | 8/10bit        | 59. 94, 60                        |  |
| Technical     |  | 3840×2160                               | YCbCr/RGB   | 4:4:4    | 8bit           |                                   |  |
| Specification |  |   | YCbCr/RGB   | 4:4:4    | 10bit          | 23. 98, 24, 30, 50                |  |
| <b>C</b> O    |  | 2560×1440                               | YCbCr/RGB   | 4:4:4    | 12bit          | 23. 98, 24, 30, 50,<br>59. 94, 60 |  |
|               | 01/  | 1000 × 1000                             | YCbCr       | 4:2:2    | 8/10bit        |                                   |  |
|               | 2K   | 1920×1200                               | YCbCr/RGB   | 4:4:4    | 8/10bit        | 23. 98, 24, 30, 50,               |  |
|               |  | 1020 × 1000                             | YCbCr       | 4:2:2    | 8/10bit        | 59. 94, 60, 100,                  |  |
|               |  | 1920×1080                               | YCbCr/RGB   | 4:4:4    | 8/10bit        | 120, 144                          |  |



|                |   |                       |                    |                |                    | 23. 98, 24, 30, 50                     |  |
|----------------|---|-----------------------|--------------------|----------------|--------------------|--|--|
|                |   | 1920×1080             | YCbCr/RGB          | 4:4:4          | 12bit              | ,<br>59. 94, 60, 100,<br>120           |  |
|                |   |                       | YCbCr              | 4:2:2          | 8/10bit            | 23. 98, 24, 30, 50                     |  |
|                | HD  | 1280×720              | YCbCr/RGB          | 4:4:4          | 8/10bit            | ,<br>59. 94, 60, 100,<br>120, 144, 240 |  |
|                | D No  | ote: Only a par       | rt of supported    | l resolutions  | are liste          | d above.                               |  |
| V4KD1 INV5101: | 1×DP1.  | .2 port               |                    |                |                    |  |  |
| Details        | <ul> <li>XDP1.2 port</li> <li>1×DP port.</li> <li>DP1.2 standard.</li> <li>A single port supports a maximum resolution of 4096×2160@60Hz and a minimum of 800×600@60Hz.</li> <li>8/10/12bit input source.</li> <li>Custom resolutions: <ul> <li>Maximum width: 8, 192 pixels (8192 × 1080@60Hz, forced by external signal, EDID unavailable).</li> <li>Maximum height: 8, 192 pixels (1024 × 8192@60Hz, forced by external signal, EDID unavailable).</li> </ul> </li> <li>HDR supported.</li> <li>Independent EDID settings, adopting EDID V1.3 standard.</li> <li>HDCP2.2 compliant, backward compatible.</li> <li>Interlaced input not supported.</li> </ul> |                       |                    |                |                    |  |  |
|                | Input   | Maximum<br>resolution | Color space        | Sampling       | Color<br>depth     | Frame rate (Hz)                        |  |
|                | Ð   |                       | YCbCr/RGB          | 4:4:4          | 12bit              | 23. 98, 24, 30                         |  |
| CU             |   | 4096×2160             | YCbCr              | 4:2:2          | 8/12bit            |  |  |
|                | 4K  |                       | YCbCr/RGB          | 4:4:4          | 8bit               | 23. 98, 30, 50,                        |  |
| Technical      |   | 3840×2160             | YCbCr              | 4:2:2          | 8/10bit            | 59. 94, 60                             |  |
| Specification  |   | 5010.12100            | YCbCr/RGB          | 4:4:4          | 8/10bit            |  |  |
| s              |   | 2560×1440             | YCbCr/RGB          | 4:4:4          | 12bit              | 23. 98, 24, 30, 50<br>,                |  |
|                |   |                       |                    |                |                    | 59. 94, 60                             |  |
|                | 2К  | 1020 × 1200           | YCbCr              | 4:2:2          | 8/10bit            | 59. 94, 60<br>23. 98, 24, 30, 50       |  |
|                | 2К  | 1920×1200             | YCbCr<br>YCbCr/RGB | 4:2:2<br>4:4:4 | 8/10bit<br>8/10bit |  |  |
|                | 2К  | 1920×1200             |                    |                |                    |  |  |



|                            | HD   | 1920×1080<br>1280×720<br>pte: Only a part  | YCbCr/RGB<br>YCbCr<br>YCbCr/RGB | 4:4:4<br>4:2:2<br>4:4:4        | 12bit<br>8/10bit<br>8/10bit | 23. 98, 24, 30,<br>50, 59. 94, 60,<br>100, 120<br>23. 98, 24, 30,<br>50, 59. 94, 60,<br>100, 120, 144,<br>240<br>above. |
|----------------------------|--|--|---------------------------------|--------------------------------|-----------------------------|---|
| X1001N022: 1×              |  |  |                                 |                                | iro motod                   |   |
| Details                    | <ul> <li>SMPT</li> <li>A sin<br/>of 7</li> <li>8/10</li> </ul> | 2G-SDI port.<br>E424M/292M stan<br>ngle port suppor<br>20×480i@59.94H<br>bit input source<br>rlaced input su | ts a maximum res<br>lz.<br>e.   | SD/HD/3G/6G/<br>olution of 409 |                             |   |
|                            | Input  | Maximum<br>resolution  | Color space                     | Sampling                       | Color<br>depth              | Frame rate<br>(Hz)  |
|                            | 12G  | 4096×2160<br>3840×2160   | YCbCr<br>YCbCr                  | 4:2:2<br>4:2:2                 | 10bit<br>10bit              | 50, 59. 94, 60  |
|                            | 6G   | 4096×2160<br>3840×2160   | YCbCr<br>YCbCr                  | 4:2:2<br>4:2:2                 | 10bit<br>10bit              | 23. 98, 24, 25,<br>29. 97, 30   |
|                            | 3G   | 1920×1080  | YCbCr                           | 4:2:2                          | 10bit                       | 50, 59. 94, 60  |
| Technical<br>Specification |  | 1920×1080p   | YCbCr                           | 4:2:2                          | 10bit                       | 23. 98, 24, 25,<br>29. 97, 30   |
| s                          | HD   | 1920×1080i   | YCbCr                           | 4:2:2                          | 10bit                       | 50, 59. 94, 60  |
| CO                         | ΠU   | 1280×720   | YCbCr                           | 4:2:2                          | 10bit                       | 23. 98, 24, 25,<br>29. 97, 30, 50,<br>59. 94, 60  |
|                            |  | 720×576i   | YCbCr                           | 4:2:2                          | 8bit                        | 50  |
|                            | en   |  |                                 |                                |                             |   |
|                            | SD   | 720×480 i  | YCbCr                           | 4:2:2                          | 8bit                        | 59. 94  |



| V4K21N1V5101:  | 1×HDM12.0 port+1×DP1.2 port   |
|----------------|---|
|                |   |
| Details        | <ul> <li>Use either 1×HDMI Type A or 1×DP port, 1×4K@60Hz input.</li> <li>HDM12.0 standard, compatible with HDM11.4/1.3.</li> <li>DP1.2 standard, compatible with DP1.1.</li> <li>A single port supports a maximum resolution of 4096×2160@60Hz and a minimum of 800×600@60Hz; the maximum pixel clock for HDM12.0 port is 594MHz.</li> <li>8/10/12bit input source.</li> <li>Custom resolutions: <ul> <li>Maximum width: 8, 192 pixels (8192 × 1080@60Hz, forced by external signal, EDID unavailable).</li> <li>Maximum height: 8, 192 pixels (1024 × 8192@60Hz, forced by external signal, EDID unavailable).</li> </ul> </li> <li>HDR supported.</li> <li>Independent EDID settings, adopting EDID V1.3 standard.</li> <li>HDCP2.2 compliant, backward compatible.</li> </ul> |
| Technical      | • Interlaced input not supported.   |
| Specification  | <ul> <li>For HDM12.0 port, please refer to the V4KH11NV5101-1×HDM12.0 port.</li> <li>For DP1.2 port, please refer to the V4KD11NV5101-1×DP1.2 port.</li> </ul>  |
| S              |   |
| X1001N0011: 4> | ×DVI ports  |
| Details        | <ul> <li>4×SL-DVI-1 ports, 4×2K@60Hz inputs.</li> <li>4×SL-DVI-1 ports, 4×2K@60Hz inputs.</li> <li>A single port supports a maximum resolution of 1920×1200@60Hz and a minimum of 800×600@60Hz; the maximum pixel clock is 165MHz.</li> <li>8bit input source.</li> <li>Custom resolutions: <ul> <li>Maximum width: 4,096 pixels (4096×512@60Hz, forced by external signal, EDID unavailable).</li> <li>Maximum height: 4,096 pixels (512×4096@60Hz, forced by external signal, EDID unavailable).</li> </ul> </li> <li>Independent EDID settings, adopting EDID V1.3 standard.</li> <li>HDCP1.4 compliant, backward compatible.</li> <li>Interlaced input not supported.</li> </ul>  |



X100 Pro-2U Specification

|                         | Input   | Maximum<br>resolution   | Color space   | Sampling   | Color<br>depth                       | Frame rate (Hz)   |
|-------------------------|---|---|---|--|--------------------------------------|---|
| Technical               |   | 1920×1200   | YCbCr   | 4:2:2  | 8bit                                 |   |
| Specification           | 2K  | 1920 × 1200   | YCbCr/RGB   | 4:4:4  | 8bit                                 | 23. 98, 24, 30, 50  |
| S                       | 21  |   | YCbCr   | 4:2:2  | 8bit                                 | ,   |
|                         |   | 1920×1080   | YCbCr/RGB   | 4:4:4  | 8bit                                 | 59. 94, 60  |
|                         | 🛱 No  | ote: Only a par   | rt of supported   | resolutions  | are liste                            | ed above.   |
| (1001 <b>N</b> 0021: 4> | <hr/> HDMI p  | orts  |   |  |                                      |   |
| Details                 | <ul> <li>HDMI</li> <li>A sin<br/>of 8</li> <li>8bit</li> <li>Cust</li> <li>M<br/>E</li> <li>M</li> <li>S</li> <li>Inde</li> <li>HDCP</li> </ul> | DMI Type A por<br>1.4 standard,<br>ngleportsuppo<br>00×600@60Hz;<br>input source.<br>om resolutions<br>laximum width: 4<br>DID unavailabl<br>laximum height<br>ignal, EDID un<br>pendent EDID s | the maximum pi<br>4,096 pixels (40<br>e).<br>: 4,096 pixels<br>havailable).<br>settings, adopt<br>backward comp | 3<br>inputs.<br>h HDMI1.3.<br>solution of 19<br>xel clock is<br>96×512@60Hz<br>s (512×4096@<br>ing EDID V1.3 | 165MHz.<br>, forced by<br>@60Hz, for | 60Hz and a minimu<br>v external signal<br>rced by externa |
|                         | Input   | resolution  | Color space   | Sampling   | depth                                | Frame rate (Hz  |
| Technical               |   | 1920×1200   | YCbCr   | 4:2:2  | 8bit                                 | 23. 98, 24, 30,   |
| Specification           | 2K  | .,20,,1200  | YCbCr/RGB   | 4:4:4  | 8bit                                 |   |
| S                       | U'  | 1920×1080   | YCbCr   | 4:2:2  | 8bit                                 | 50, 59. 94, 60  |
| C(Y)                    |   |   | YCbCr/RGB   | 4:4:4  | 8bit                                 |   |
|                         |   | te Only a nau   | rt of supported   | rocolutiono  | ara liata                            | d above   |



| X1001N018: 4×              | VGA por  | ts   |                  |                     |                |                    |  |
|----------------------------|--|--|------------------|---------------------|----------------|--------------------|--|
| Details                    | <ul> <li>A sin<br/>of 64</li> <li>8bit</li> <li>Custo</li> </ul>   | GA ports, 4×2k<br>gle port suppor<br>40×480@60Hz.<br>input source.<br>om resolutions:<br>aximum width: 1 | ts a maximum res | 3<br>solution of 19 | 920×1080@      | 60Hz and a minimum |  |
|                            | <b>-</b> Ma  | aximum height:   | 1,080 pixels (   | 1080×1920@6         | OHz).          |                    |  |
| Technical                  | Input  | Maximum<br>resolution  | Color space      | Sampling            | Color<br>depth | Frame rate (Hz)    |  |
| Specification              | 2K   | 1920×1080  | RGB              | 4:4:4               | 8bit           | 59. 94, 60         |  |
| S                          | No <sup>-</sup>  | te: Only a part  | t of supported   | resolutions         | are listed     | above.             |  |
| Details                    | <ul> <li>VGA ports + 2× CVBS ports</li> <li>2×VGA ports, 2×CVBS ports.</li> <li>A single VGA port supports a maximum resolution of 1920×1080@60Hz and a minimum of 640×480@60Hz.</li> <li>Maximum width: 1,920 pixels (1920×1080@60Hz).</li> <li>Maximum height: 1,080 pixels (1080×1920@60Hz).</li> <li>A single CVBS port supports PAL/NTSC video standard.</li> <li>8bit input source.</li> </ul> |  |                  |                     |                |                    |  |
| Technica                   | Input  | Maximum<br>resolution  | Color space      | Sampling            | Color<br>depth | Frame rate (Hz)    |  |
| Technical<br>Specification | SD   | 720×576i   | YCbCr            | 4:2:2               | 8bit           | 50                 |  |
| Specification              | <b>U</b>   | 720×480i   | YCbCr            | 4:2:2               | 8bit           | 59.94              |  |
| S                          |  | te:Onlyapart<br>ports,please   |                  |                     | ·              | are listed above.  |  |



| X1001N004: 4×      | SDI por   | ts                              |                |                |                |  |  |
|--------------------|---|---------------------------------|----------------|----------------|----------------|--|--|
| Details            | <ul> <li>4×3G-SDI ports, 4×2K@60Hz inputs.</li> <li>A single port supports a maximum resolution of 1920×1080@60Hz.</li> <li>SMPTE424M/292M standard; supports SD-SDI/HD-SDI/3G-SDI (Level A/B).</li> <li>8/10bit input source.</li> <li>Interlaced input supported: 1080i/480i/576i.</li> </ul> |                                 |                |                |                |  |  |
|                    | Input   | Resolution                      | Color<br>space | Sampling       | Color<br>depth | Frame rate (Hz)                                  |  |
|                    | 3G  | 1920×1080                       | YCbCr          | 4:2:2          | 10bit          | 50, 59. 94, 60                                   |  |
|                    |   | 1920×1080p                      | YCbCr          | 4:2:2          | 10bit          | 23. 98, 24, 25,<br>29. 97, 30                    |  |
| Technical          | HD  | 1920×1080i                      | YCbCr          | 4:2:2          | 10bit          | 50, 59. 94, 60                                   |  |
| Specification<br>s | Ц   | 1280×720                        | YCbCr          | 4:2:2          | 10bit          | 23. 98, 24, 25,<br>29. 97, 30, 50,<br>59. 94, 60 |  |
|                    |   | 720×576i                        | YCbCr          | 4:2:2          | 8bit           | 50   |  |
|                    | SD  | 720×480i                        | YCbCr          | 4:2:2          | 8bit           | 59.94  |  |
|                    |   | te: 3G-SDI port s<br>ted above. | upports Level  | A/B. Only a pa | art of supp    | orted resolutions                                |  |
| VIPX2V2001: V_     | IPX2 po   | rts                             |                |                |                |  |  |
| Details            | <ul> <li>2×RJ45 GbE ports; 1×USB 3.0 port.</li> <li>4. 264/H. 265 decoding supported.</li> <li>0NVIF, GB28181, RTSP and other protocols supported.</li> <li>DHCP supported.</li> <li>Supports firmware upgrades for V_IPX2 decoder cards via a USB drive.</li> </ul>                            |                                 |                |                |                |  |  |
|                    | 8 Channe  | els, 3840×2160@                 | 30fps          |                |                |  |  |
| Technical          | 18 Chan   | nels, 2560×1440                 | @30fps         |                |                |  |  |
| Specification      | 32 Chan   | nels, 1920×1080                 | @30fps         |                |                |  |  |
| S                  | 64 Chan   | nels, 720×576@3                 | 80fps          |                |                |  |  |
|                    | 🛱 Not   | te: Only a part                 | of supported   | resolutions    | are listed     | d above.   |  |



### 5.2 Output Board

| X1000UT03: 8> | <ethernet ports<="" th=""></ethernet>  |
|---------------|--|
|               |  |
| Details       | <ul> <li>8×RJ45 GbE ports; a single board supports up to 5.24 million pixels.</li> <li>Load capacity of a single board: <ul> <li>8bit@60Hz: 5.24 million pixels; 10bit@60Hz: 3.93 million pixels</li> <li>8bit@120Hz: 2.62 million pixels; 10bit@120Hz: 1.96 million pixels</li> <li>8bit@240Hz: 1.31 million pixels; 10bit@240Hz: 0.98 million pixels</li> </ul> </li> <li>Load capacity of a single Ethernet port: <ul> <li>8bit@60Hz: 0.65 million pixels; 10bit@60Hz: 0.49 million pixels</li> <li>8bit@120Hz: 0.32 million pixels; 10bit@60Hz: 0.24 million pixels</li> <li>8bit@240Hz: 0.16 million pixels; 10bit@240Hz: 0.12 million pixels</li> <li>8bit@240Hz: 0.16 million pixels; 10bit@240Hz: 0.12 million pixels</li> </ul> </li> <li>Indicator status (2 indicators per port): <ul> <li>Steady on (power indicator): Normal power supply.</li> <li>Blinking (data indicator): Normal signal output.</li> </ul> </li> </ul>   |
| X1000UT04: 10 | ×Ethernet ports  |
| Details       | <ul> <li>IOXTI FORT2 FORT3 FORT4 FORT5 FORT6 FORT7 FORT9 FORT9 FORT10</li> <li>10×RJ45 GbE ports; a single board supports up to 6.55 million pixels.</li> <li>Load capacity of a single board: <ul> <li>8bit@60Hz: 6.55 million pixels; 10bit@60Hz: 4.91 million pixels</li> <li>8bit@120Hz: 3.27 million pixels; 10bit@120Hz: 2.45 million pixels</li> <li>8bit@240Hz: 1.63 million pixels; 10bit@240Hz: 1.22 million pixels</li> </ul> </li> <li>Load capacity of a single Ethernet port: <ul> <li>8bit@120Hz: 0.32 million pixels; 10bit@60Hz: 0.49 million pixels</li> <li>8bit@240Hz: 0.16 million pixels; 10bit@240Hz: 0.24 million pixels</li> <li>8bit@240Hz: 0.16 million pixels; 10bit@240Hz: 0.12 million pixels</li> </ul> </li> </ul> |

| X100PR0V1001: | $4 \times 5$ G Ethernet ports  |
|---------------|--|
|               |  |
| Details       | <ul> <li>4×5G Ethernet ports (2 main and 2 backup), with a maximum load capacity of 5.89 million pixels per port.</li> <li>Works with Cat6A shielded cables, with a transmission distance of 100m.</li> <li>Automatic backup, no configuration required. Ports 1&amp;2 serve as the main output ports, while port 3 automatically backs up data from port 1, and port 4 automatically backs up data from port 2.</li> <li>Load capacity of a single board: <ul> <li>8bit@60Hz: 5.89 million pixels; 10bit@60Hz: 4.42 million pixels</li> <li>8bit@120Hz: 2.94 million pixels; 10bit@20Hz: 2.21 million pixels</li> <li>8bit@240Hz: 1.47 million pixels; 10bit@20Hz: 2.21 million pixels</li> <li>8bit@60Hz: 2.94 million pixels; 10bit@60Hz: 2.21 million pixels</li> <li>8bit@60Hz: 1.47 million pixels; 10bit@20Hz: 1.10 million pixels</li> <li>8bit@120Hz: 1.47 million pixels; 10bit@20Hz: 2.55 million pixels</li> <li>8bit@240Hz: 0.73 million pixels; 10bit@20Hz: 0.55 million pixels</li> </ul> </li> <li>Indicator status (2 indicators per port): <ul> <li>Steady on (power indicator): Normal power supply.</li> <li>Blinking (data indicator): Normal signal output.</li> </ul> </li> </ul> |

Coloillo



| X1000UT05: 2×1 | Fiber port                                 | S                                 |            |                |               |   |
|----------------|--|-----------------------------------|------------|----------------|---------------|---|
|                |  |                                   |            | 2              |               |   |
|                |  | fiber ports (1<br>ith a dedicate  |            | •              | eiver. Eac    | ch fiber port can be                    |
|                | convert                                    | ed to 10×GbE p                    | ports.     |                |               |   |
|                | -  |                                   | -          |                |               | with a transmission                     |
| Details        |  | modules (optio                    | -          |                | . It can a    | also work with other                    |
|                | • Automat                                  | ic backup, noco                   | onfigurati | on required. F | Port1serv     | ves as the main output                  |
|                |  | •                                 | the backu  | p port which   | n backs u     | p data from port 1                      |
|                | <ul><li>automat</li><li>Load car</li></ul> | ically.<br>pacity of port         | 1:         |                | N             |   |
|                |  | @60Hz: 6.55 mi                    |            | ls; 10bit@60H  | Hz: 4.91 m    | illion pixels                           |
|                |  |                                   |            |                |               | million pixels                          |
|                |  |                                   |            |                |               | million pixels<br>levice control range. |
| X1000UT18: HDM | -  | 0                                 |            |                |               | J                                       |
|                | OUTPUT                                     |                                   | HDMI 2.0   |                | 4             | K INT                                   |
|                |  | 2.0 port, 1×4                     |            |                | 6 400 ( >> 24 |   |
|                |  | 600@60Hz.                         |            | resolution o   | 1 4090 ~ 21   | 60@60Hz and a minimum                   |
| Details        | • Custom                                   | output resolut                    | ions:      |                |               |   |
|                |  | mum width: 8,19                   |            |                |               |   |
|                |  | num height: 8,′<br>output support | •          | (1024 × 81880  | ₩6UHZ).       |   |
|                | • The out                                  | put image of ea                   | ach port c | an be set fre  | ely within    | n the device control                    |
|                | range.                                     | 4 (100-0                          |            |                |               |   |
|                |  | :4/YCbCr 4:2:2                    | Color      |                | Color         |   |
|                | Output                                     | Resolution                        | space      | Sampling       | depth         | Frame rate (Hz)                         |
|                | 4K   | 4096×2160                         | RGB        | 4:4:4          | 8bit          | 30, 59. 94, 60                          |
| Technical      |  | 3840×2160                         | RGB        | 4:4:4          | 8bit          | 20.50.01.40.400                         |
| Specifications | 2K   | 1920×1200                         | RGB        | 4:4:4          | 8bit<br>10bit | 30, 59. 94, 60, 100,<br>119, 120, 144   |
|                | Others                                     | 1720 / 1000                       | YCbCr      | 4:2:2          | 10bit         | 30, 59, 94, 60, 100,<br>119, 120, 144   |
|                |  |                                   |            |                |               | · · ·                                   |



|   | <u></u>   |                       |                |                 |                |                               |  |
|---|---|-----------------------|----------------|-----------------|----------------|-------------------------------|--|
|   | Note:   | Only a part of        | supported      | resolutions are | listed abo     | ove.                          |  |
| X1000UT01: 4×1  | DVI ports   |                       |                |                 |                |                               |  |
| Details   | <ul> <li>4×DVI ports</li> <li>4×DVI ports, 4×2K@60Hz outputs.</li> <li>A single port supports a maximum resolution of 1920×1200@60Hz and a minimum of 800×600@60Hz.</li> <li>Custom output resolutions: <ul> <li>Maximum width: 4,096 pixels (4096×512@60Hz).</li> <li>Maximum height: 4,096 pixels (512×4096@60Hz).</li> </ul> </li> <li>The output image of each port can be set freely within the device control range.</li> <li>8bit, RGB 4:4:4 output by default.</li> </ul> |                       |                |                 |                |                               |  |
| Technical<br>Specifications   | Output  | Maximum<br>resolution | Color<br>space | Sampling        | Color<br>depth | Frame rate (Hz)               |  |
|   | 2К  | 1920×1200             | RGB            | 4:4:4           | 8bit           | 29. 97, 59. 94, 30,<br>50, 60 |  |
|   |   | 1920×1080             | RGB            | 4:4:4           | 8bit           | 29. 97, 59. 94, 30,<br>50, 60 |  |
|   | Note: Only a part of supported resolutions are listed above.  |                       |                |                 |                |                               |  |
| X1000UT02: 4×1  | HDMI ports  |                       |                |                 |                |                               |  |
| <ul> <li>A single port supports a maximum resolution of 1920×1200@60Hz and a minimum of 800×600@60Hz.</li> <li>Custom output resolutions: <ul> <li>Maximum width: 4,096 pixels (4096×512@60Hz).</li> <li>Maximum height: 4,096 pixels (512×4096@60Hz).</li> </ul> </li> <li>The output image of each port can be set freely within the device control range.</li> <li>8bit, RGB 4:4:4 output by default.</li> </ul> |   |                       |                |                 |                |                               |  |



X100 Pro-2U Specification

| Technical<br>Specifications | Output   | Maximum<br>resolution | Color<br>space | Sampling | Color<br>depth | Frame rate (Hz)               |
|-----------------------------|--|-----------------------|----------------|----------|----------------|-------------------------------|
|                             | 2K   | 1920×1200             | RGB            | 4:4:4    | 8bit           | 29. 97, 59. 94, 30,<br>50, 60 |
|                             |  | 1920×1080             | RGB            | 4:4:4    | 8bit           | 29. 97, 59. 94, 30,<br>50, 60 |
|                             | Note: Only a part of supported resolutions are listed above. |                       |                |          |                |                               |

### 5.3 Preview and Monitoring Board

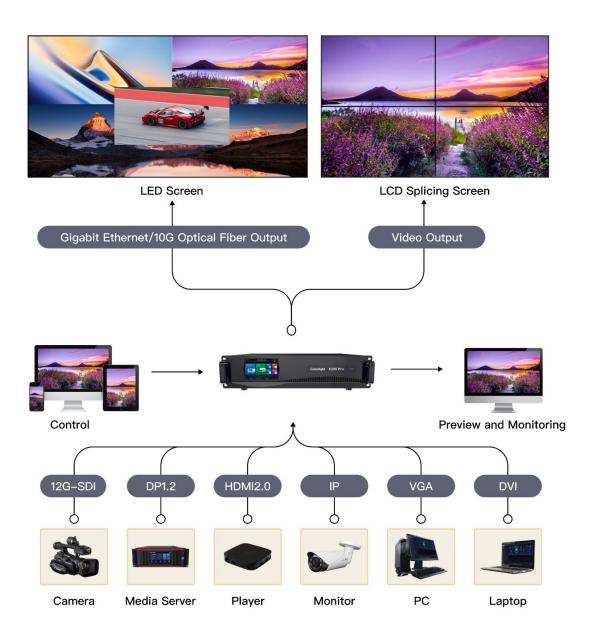
| X100PR0V2001: | Preview and monitoring board  |
|---------------|---|
| Details       | <ul> <li>1×HDMI1.4 port, for previewing inputs and monitoring outputs, with a fixed output of 1920×1080@60Hz.</li> <li>Supports previewing inputs and monitoring outputs via web-based software.</li> </ul> |

### 5.4 Main Board

| VMBRK39V2001: | Main board  |
|---------------|---|
| Details       | <ul> <li>ANTI Doard</li> <li>Intervent and intervent and active 3D glasses (optional accessories).</li> </ul> |



### 6 Applications



 $\square$  Note: The image shown is for illustration purpose only. Please refer to the actual product.

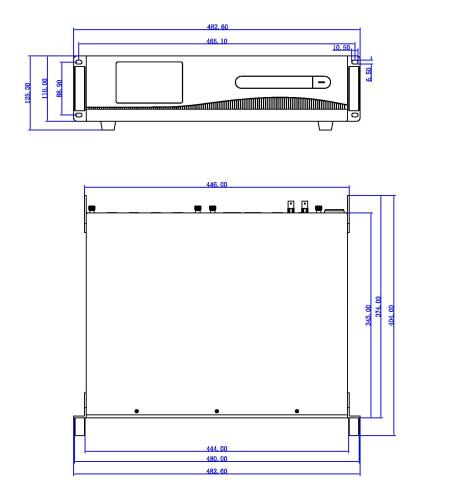
# 7 Device Specifications

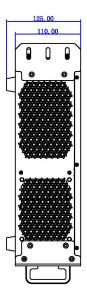
| Model                    |  | X100 Pro-2U   |  |
|--------------------------|--|---|--|
| Chassis                  |  | 2U  |  |
| Dimensions               | Device<br>(W×H×D)                      | 482.6mm (19.0")×110.0mm (4.3")×404.0mm (15.9")  |  |
|                          | Packing<br>(W×H×D)                     | 590. 0mm (23. 2") $\times$ 223. 0mm (8. 8") $\times$ 470. 0mm (18. 5")                        |  |
| Weight                   | Net                                    | 9.5kg (20.951bs)  |  |
| weight                   | Gross                                  | 11.38kg (25.091bs)  |  |
|                          | Power supply                           | AC 100 <sup>~</sup> 240V, 50/60Hz   |  |
| Electrical<br>parameters | Average board<br>power                 | 1 OW  |  |
|                          | Maximum power<br>of a single<br>device | 110. 8W   |  |
| Operating                | Temperature                            | 10° C~50° C / 50°F~122°F  |  |
| environment              | Humidity                               | 0%RH <sup>~</sup> 85%RH, non-condensing   |  |
| Storage                  | Temperature                            | -10° C~60° C / 14°F~140°F   |  |
| environment              | Humidity                               | 0%RH <sup>~</sup> 95%RH, non-condensing   |  |
| Placement conditions     |  | This device can only be placed horizontally. Do not invert the device or place it vertically. |  |

www.colorlightinside.com



### 8 Reference Dimensions







#### Statement

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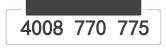
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